

FIG. 1

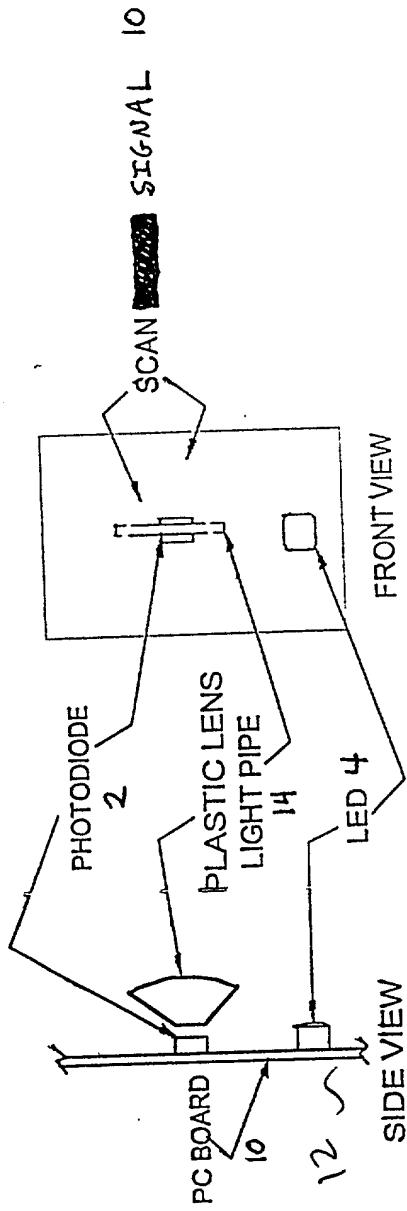


FIG. 2

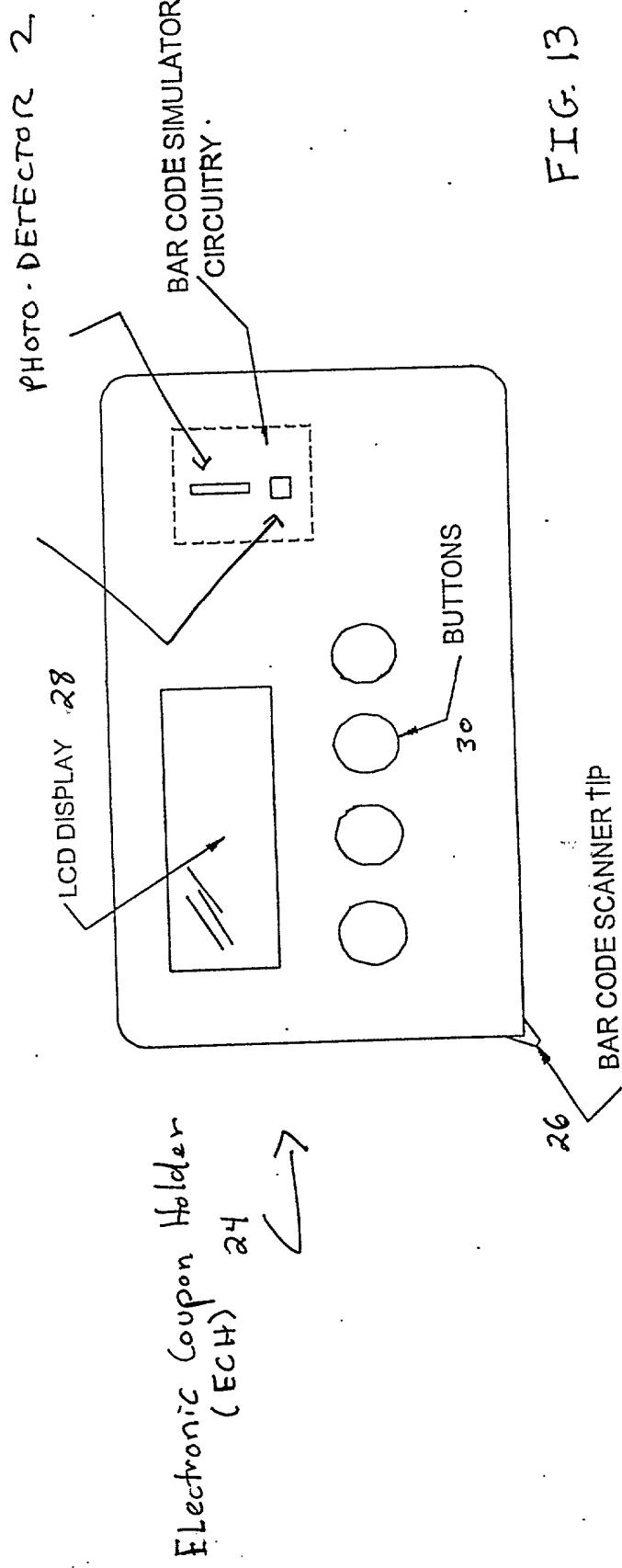
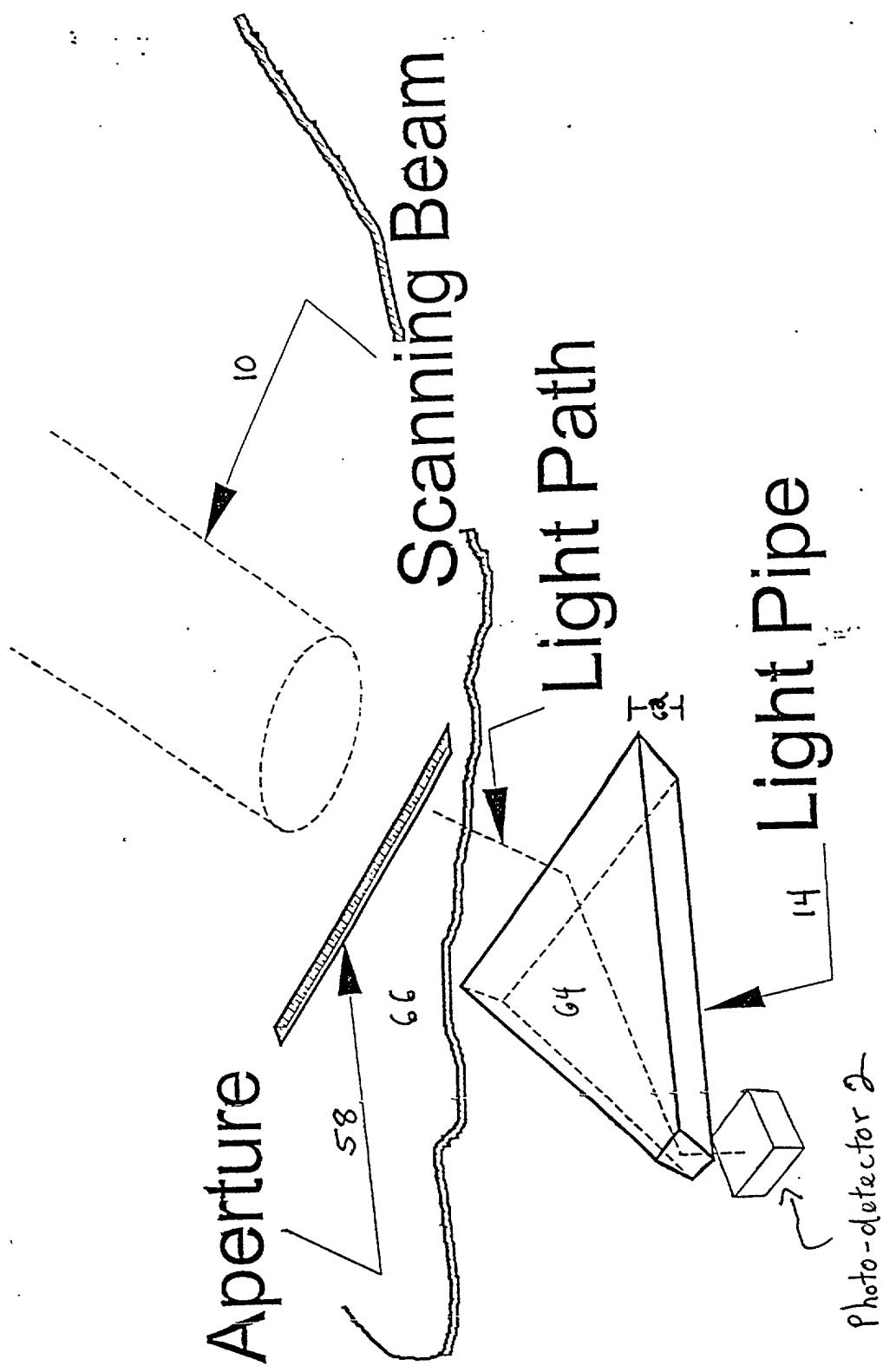


FIG. 3



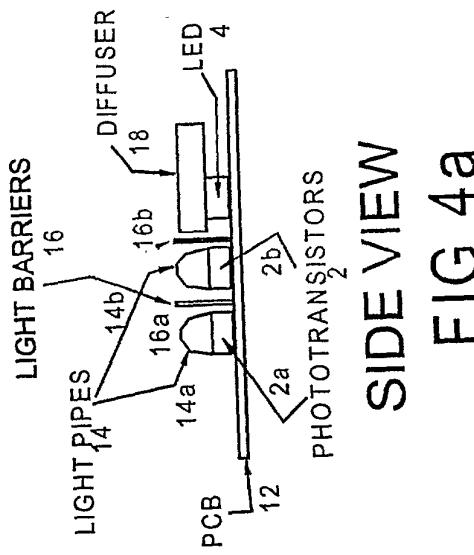


FIG 4a  
SIDE VIEW

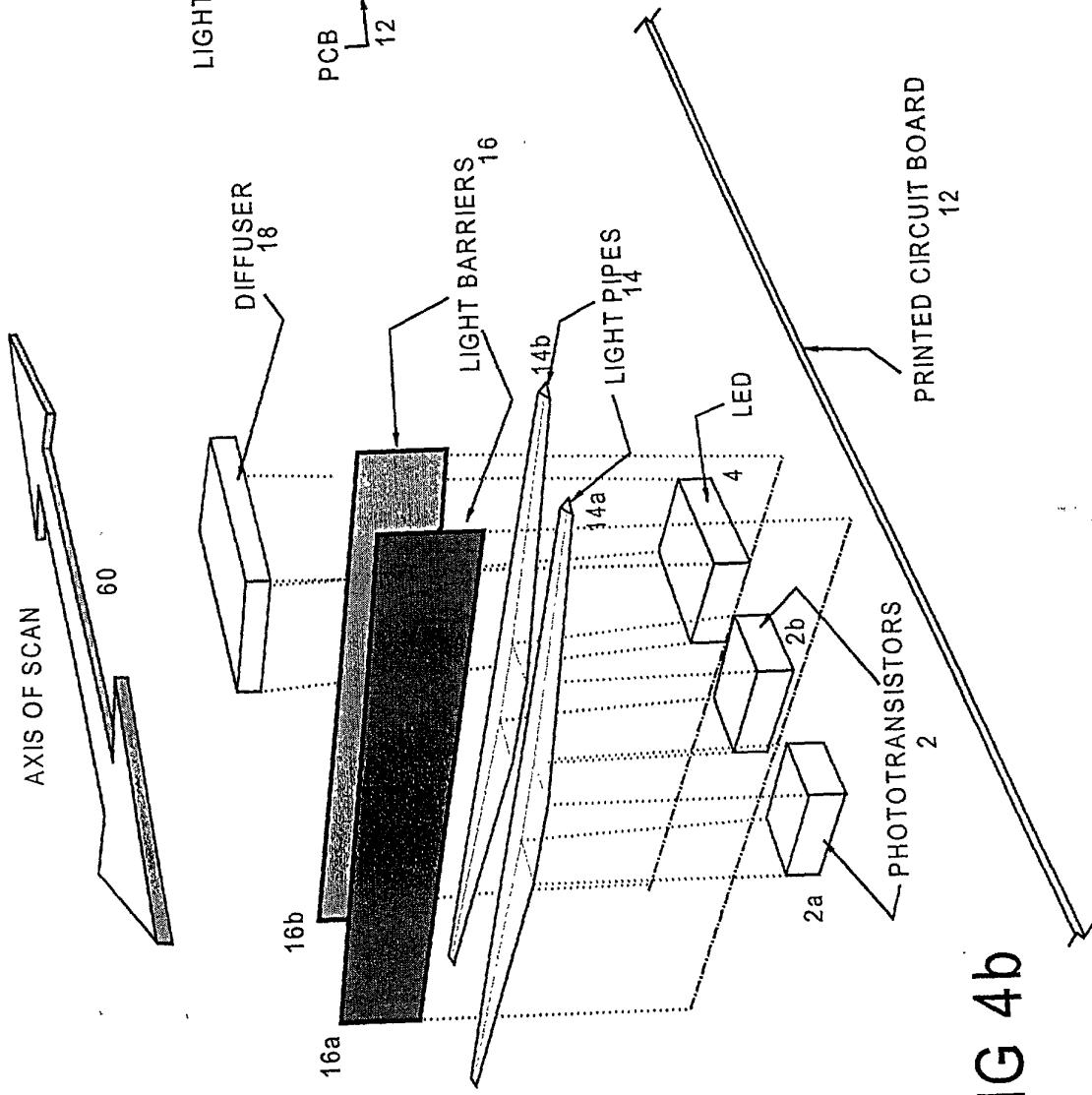


FIG 4b

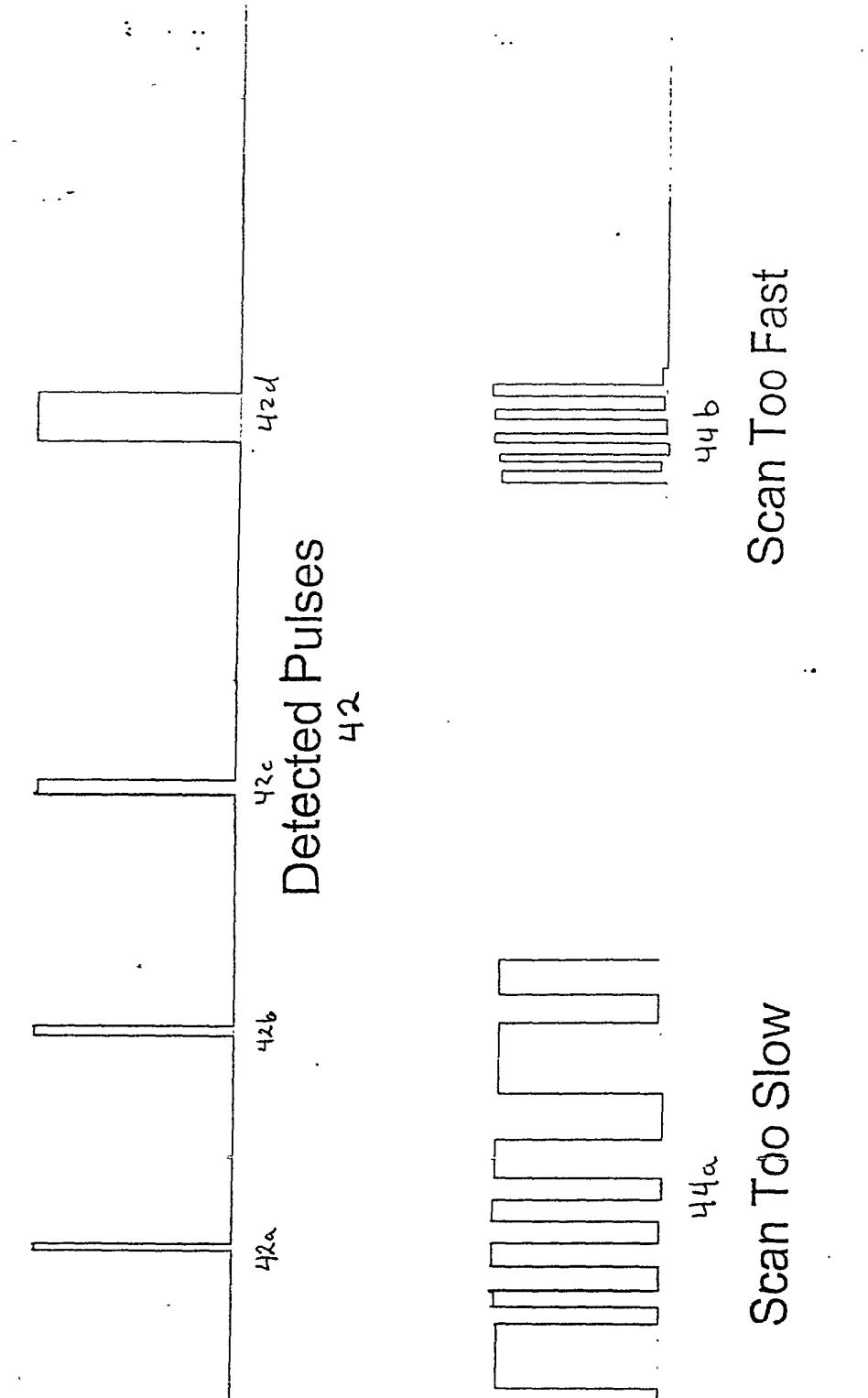


FIG. 5a

FIG. 5b

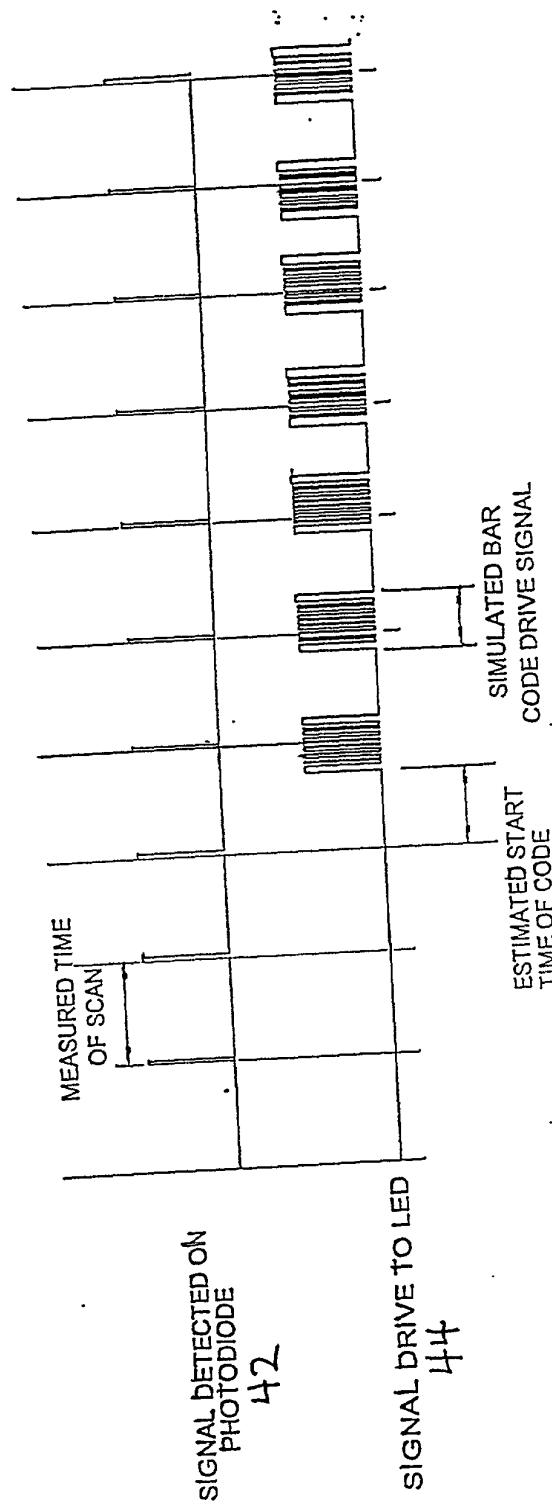
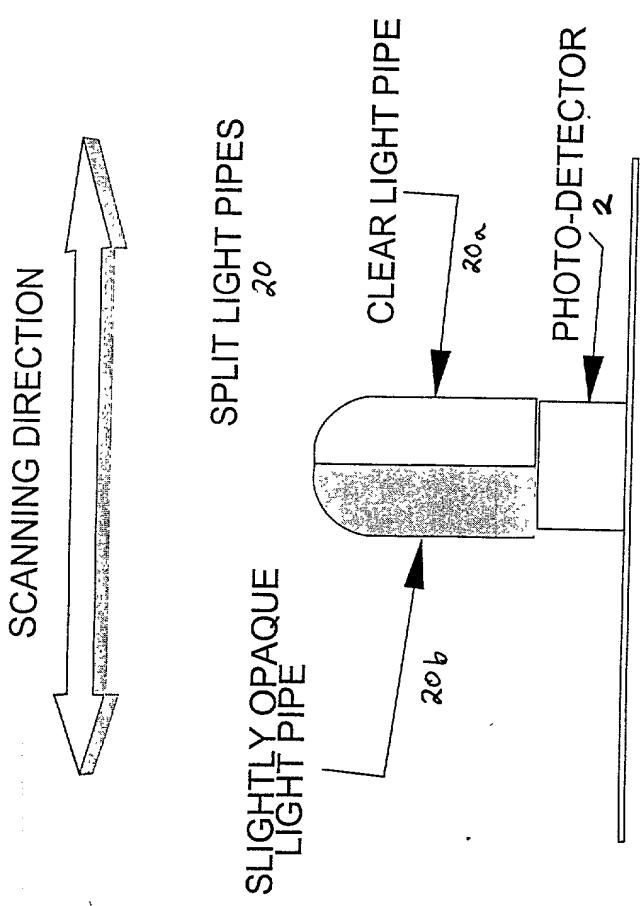
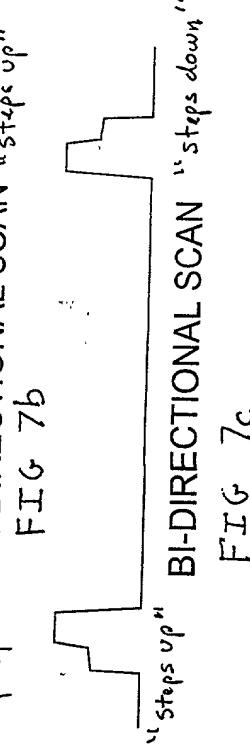
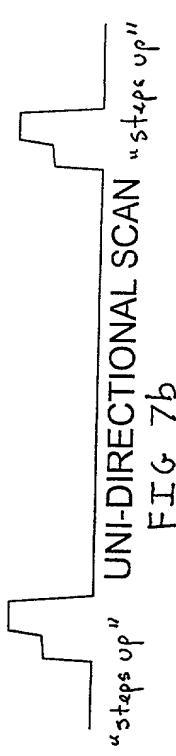


FIG. 6



SIDE VIEW DETECTOR ASSEMBLY  
FIG. 7a



"steps up"  
FIG. 7b

"steps up"  
FIG. 7c

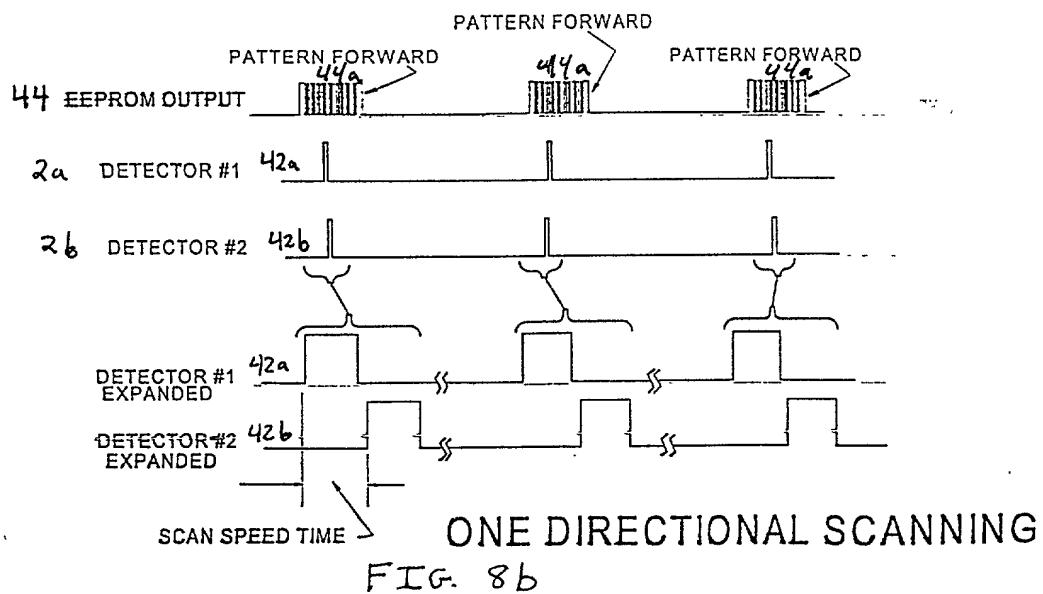
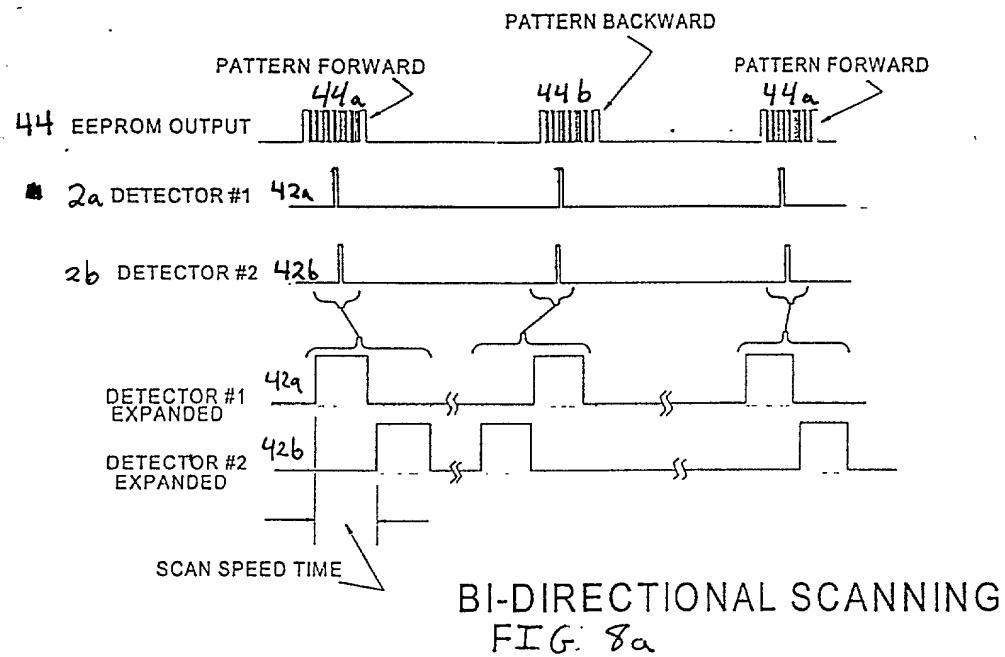
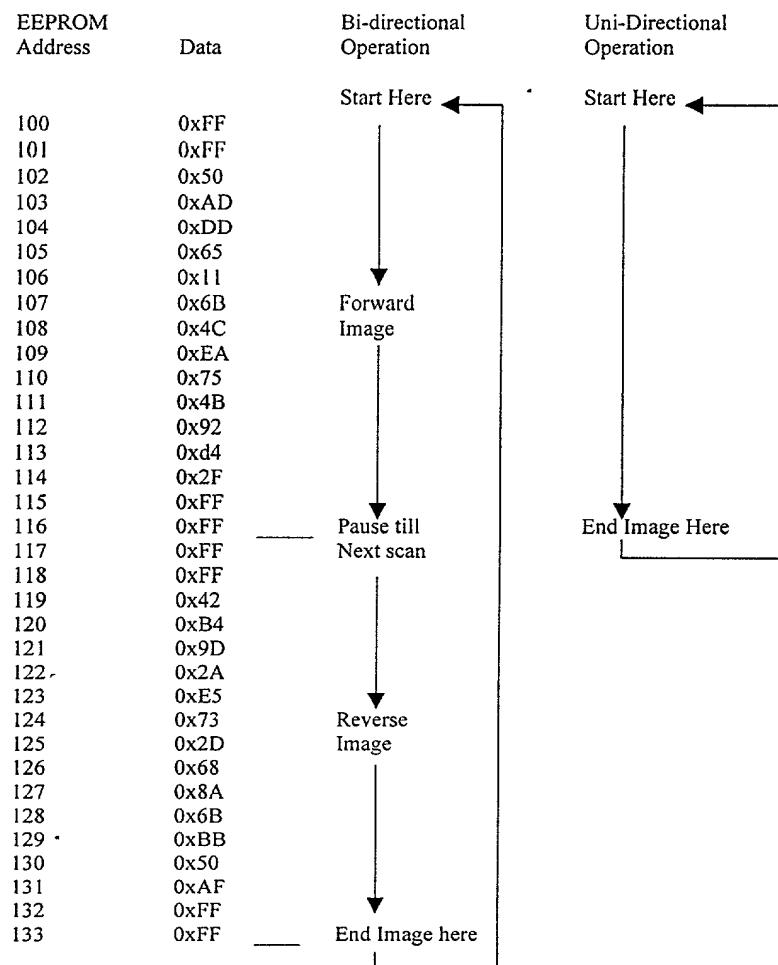


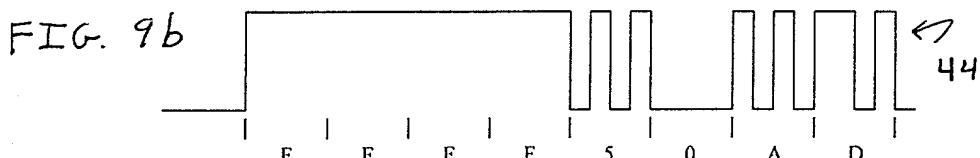
FIG. 9a



Clock Intervals

56

Image Data



## FAST CLOCKING

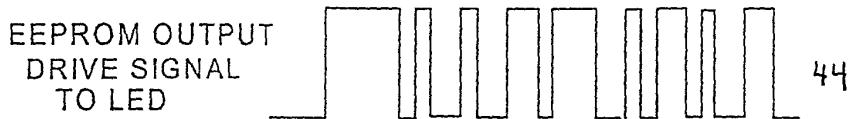


FIG 10a

## SLOW CLOCKING



FIG 10b

09566034 09566034

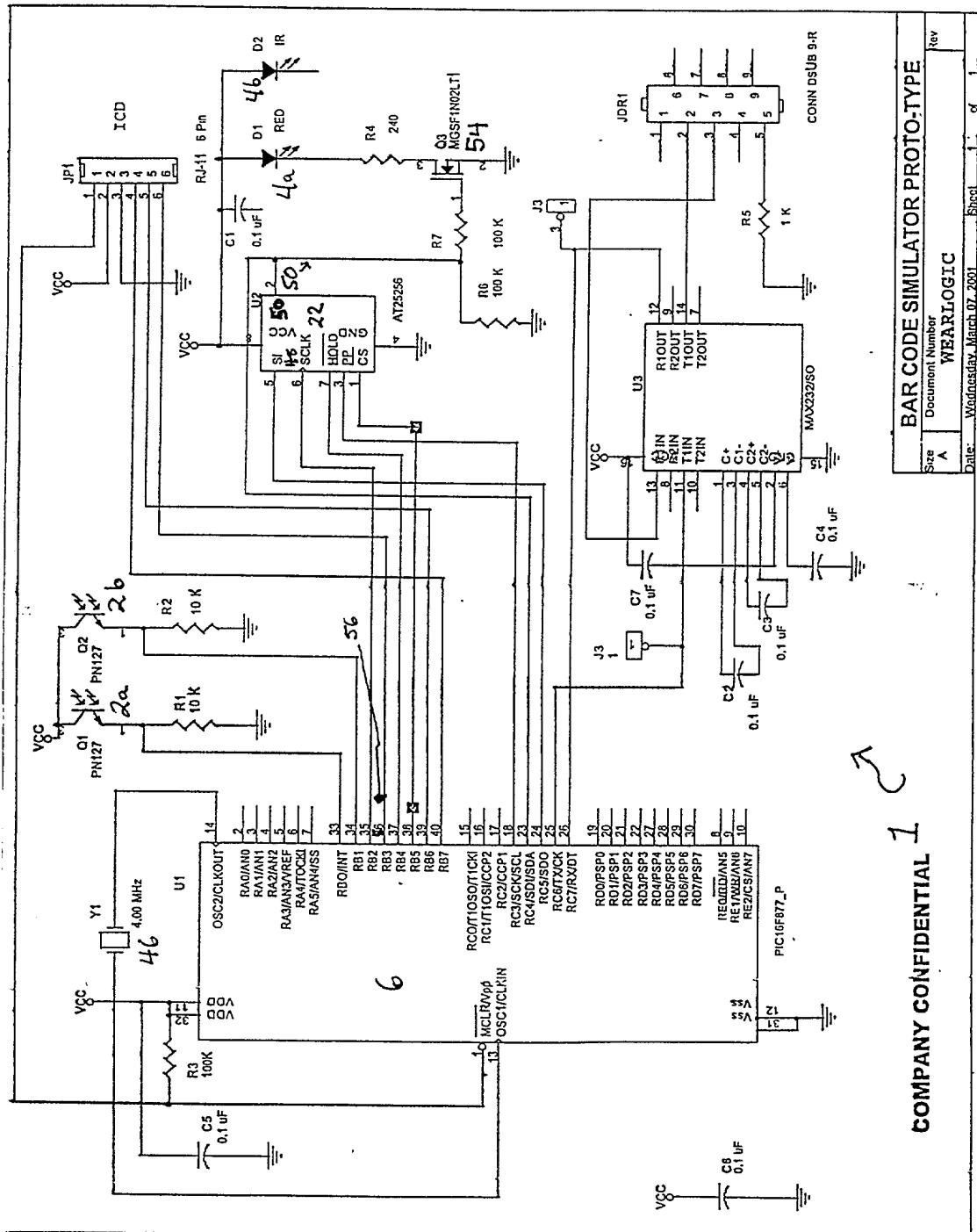


FIG. 11

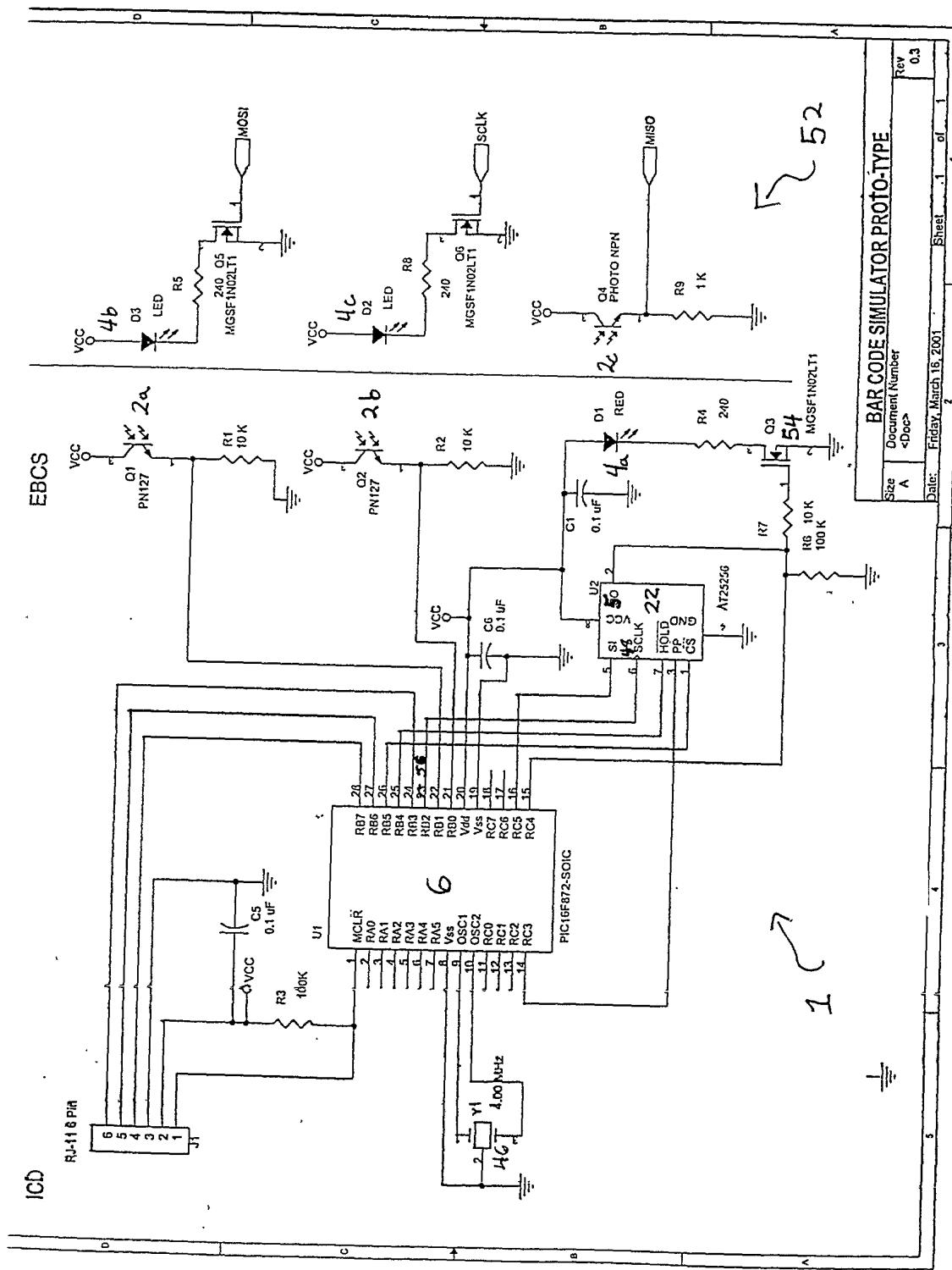


FIG. 12

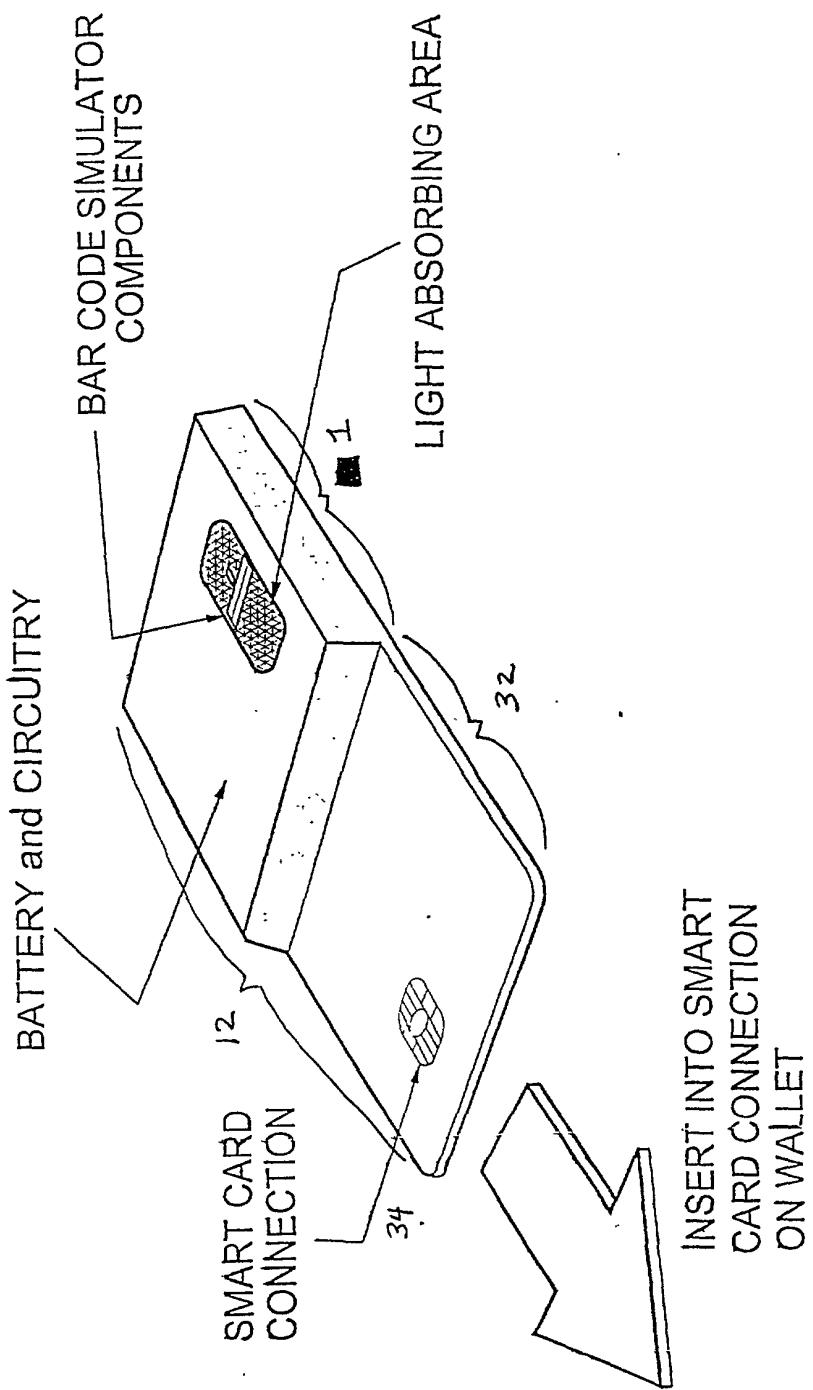


FIG. 14

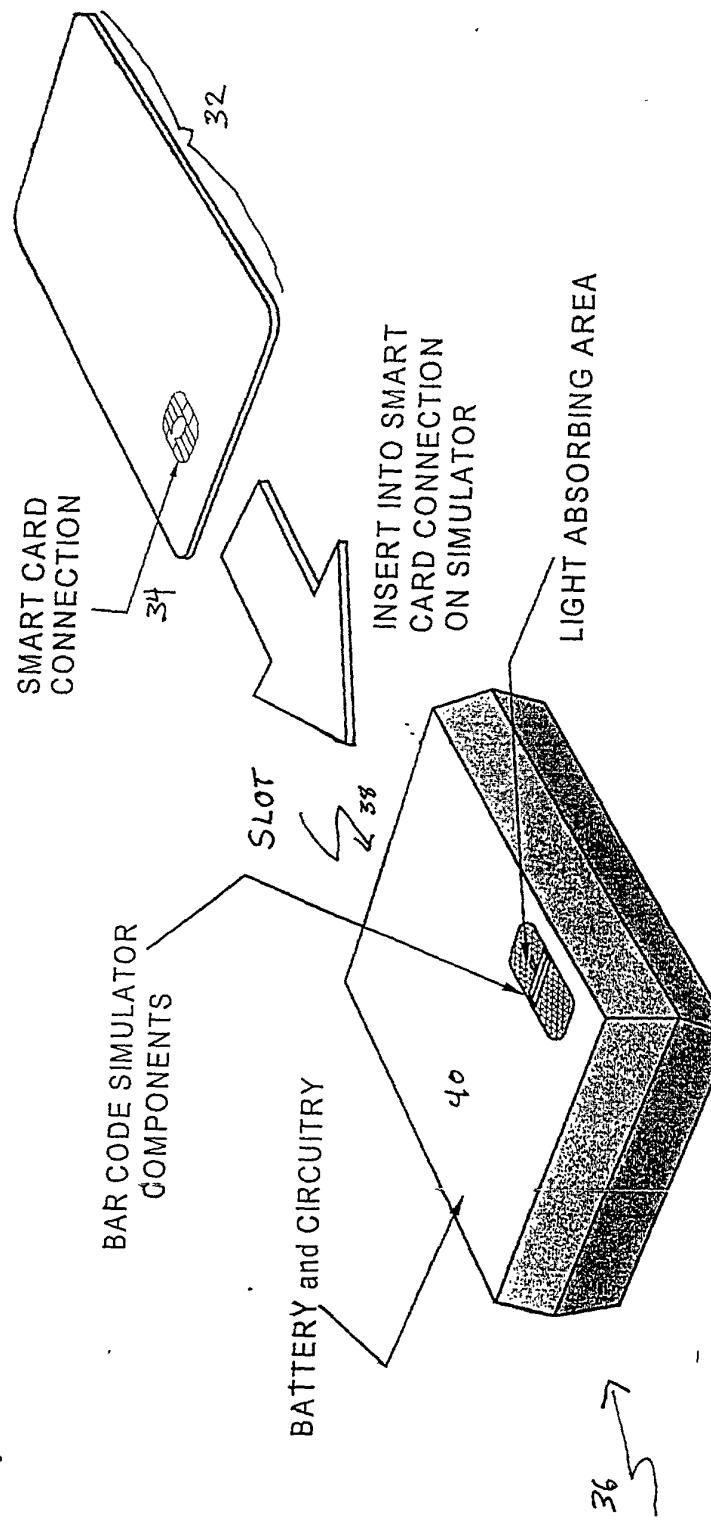


FIG. 15